**Concurrent Programming ID1217**

Course Summary

**Module I: Parallel Programming Concepts, Models, and Paradigms**

**Q**. Propose software implementation of shared memory abstraction.

**A**. One possible solution is to have one centralized server which provides the illusion of a shared memory for the nodes. The server is accessed thru message passing or RPC similar to a remote file server.

Each call to the server specifies a shared location, operation and the data in case of a store op. All accesses to the server are serialized.

The downsides of this solution is the server is a single point of failure and cannot easily be scaled.

Another possible solution is a P2P DHT on a set of nodes with a key value store that is more robust and scalable.

**Amdahl’s law:**

The speedup that can be achieved by executing a program in parallel is estimated by utilizing Amdahl’s law.

Given a fixed problem size: Speedup (n processors) =

The limiting factor of the speedup is the sequential parts of the program that cannot be parallelized due to data dependicies.

Amdahl’s law states that the speedup that can be achieved by executing a parallel program on n processors is equal to:

Where s is the serial fraction of the serialized execution. The speedup is limited by when n approches infinity.

Example: if 10% of a program is serial the achieveable speedup on 10 processors are:

= 5,26. While the maximum theoretical speedup on processors are

**Q**. Describe three distributed programming paradigms:

Iterative parallelism, recursive parallelism and pipeline parallelism.

**Iterative parallelism:**

Parallelization of independent iterations executed concurrently in seperated threads of processes. Example is iterative matrix multiplication.

**Recursive parallelism:**

Parallelization of independent function calls i.e. making function calls in parallel. ‘

Often based on sequential programs that utilizes divide and conquer algorithms.

Example is parallel sort.

**Pipeline parallelism:**

Parallelization of producers and consumers i.e. parallelism of producing and consuming data.

**Q**. Propose and describe a software implementation of the synchronous and asynchronous message passing programming model in a shared memory multiprocessor.

**A**. The idea is to use a set of shared buffers to send and receive messages by storing/fetching to/from the buffers. The buffers can be provided in the system address space i.e. in the kernel. Buffers can be implemented using a data structure monitor that operates as a queue to store/load messages where a synchronous message passing can be used with a synchronous queue that is a blocking queue in which each put must wait for take and vice versa.

Implementation of message passing using shared memory is a consumer-producer problem and any solution to this problem can also be used to implement messsage passing.

**Module II: Processes and Synchronization**

**Q**. Explain interleaving semantics and non-determism of concurrent execution. Indicate major reason for non-determism in concurrent execution. Give examples of non-determism in concurrent execution.

**A**.

**Interleaving semantics:** The concurrent execution of a parallel program by several processes can be viewed as the interleaving of execution histories of the processes.

**Non-determism:** The behaviour of a concurrent program is not reproducible as different histories can be observed on different executions due to different execution rates of processes and dynamism of the execution environment.

Example: Concurrent increments of an unprotected shared counter by concurrent threads.

**Q**. Explain why and how synchronization together with a global invariant defined for a parallel application helps to develop a correct concurrent program.

**A**.

Global invariant (GI) is a predicate that is true in every visible program state – namely before and after every atomic action. It captures the relations between values of global and local variables as well as constraints on possible values of the variables.

Synchronization is an interaction between processes that controls the order in which the processes execute. There are two types of synchronization, mutual exclusion and condition synchronization.

In general, synchronization delays a process until it can proceed safely so that the program does not enter a bad state. By synchronizing processes the set of possible execution histories can be reduced to those in which the GI holds true.

**Q**. Name and define to major types of synchronization.

**A**. Mutual exclusion is used to gurantee that a shared resource / critical section is only accessed by one thread / process at a time. This is done by protecting the resource with a lock or semaphore that needs to be locked before accesing the resource. After the critical section has been accessed the lock is then unlocked allowing another thread to take the lock.

A example of this is if n >= 2 processes needs to update a shared counter then the incrementing operation becomes a critical section that must be executed by one process at a time with mutual exclusion.

Conditional synchronization on the other hand allows delaying a process until a certain condition becomes true. This is done by signal() and wait(). Example is the producer / scheme were the two waits for the other before proceeding.

**Q**. Explain differences between heavy-wait process and thread.

**A**.

A heavy-weight process executes in its own (virtual) address space that is completely isolated from address spaces of other HWPs. A HWP can create another HWP that is an exact copy of the parent but executes in its own address space. Normally, processes are scheduled and controlled by the OS kernel. A thread is essentially a program counter, a set of registers. and a stack used to store its private local variables, pass parameters to functions and return values. Threads are created in a HWP and share the heap. local variables, and the text segment (code), which belongs to the process. Threads ("green threads”) can be scheduled and controlled by a run-time system in the user mode.

Q. Explain pros and cons of Shared single ready list vs one list per processor.

|  |  |
| --- | --- |
| Shared single RL: | + automatic load balancing  - contention for shared lock  - processes executes on different cores -> cold cache |
| RL per processor | -poor load balancing  + less lock contention  + processes executes on same processor -> warm cache |

A hybrid strategy would be to in case of empty RL on one processor “steal” a process from the tail of another cores RL. In this way we combines load balancing with warm caches.

Disadvantages is higher contention for ready list but can be solved by stealing and dealing processes from tail and head.

**Module III: Critical Sections. Locks and Condition Variables**

**Q**. Describe properties and possible implementations of two different locking algorithms.

**A**. Test / Set lock and Ticket lock:

**Test and set** lock is an algorithm that returns the old value of a memory location and set the memory location to 1 as a single atomic operation. Lock value of 0 means the critical section is vacant and no process in it. Value of 1 means the critical section is being occupied.

**Ticket lock** is a synchronization lock that uses tickets to control which threads that is allowed to enter a critical section. The basic concept is similar to many ticket queue systems were customers / threads are served in the order they arrive. Like this, ticket lock is a FIFO queue based mechanism. It adds the benefit of fairness and works as follows.

There are two integer values, queue ticket and dequeue ticket. The first represents the threads position in the queue and the latter is the ticket. When a thread arrives it atomically obtains and increments the queue ticket. It then compares its ticket value with the dequeue ticket and if equal the thread can enter the CS. When a thread leaves the CS it must increment the dequeue ticket.

**Q**. Describe the covering condition technique used in condition synchronization.

**A**. Covering condition technique is typically used with monitors. A process signals a condition variable letting waiting threads know they can be proceed. The covering condition might be weaker than the actual condition the threads waiting for.

The covering condition technique is worth using when processes synchronize on many different conditions. Instead of signaling specific condition one “covering” condition that is a disjunction of the actual conditions is signalled to the waiting processes. This allows for reducing the number of condition variables used on the cost of false alarms.

The conditions are instead re-eveluated, this can be done in parallel by the signaled processes rather than sequentially by the signalling process. However, it results in some overhead of false alarms.

For each of the following two options, show implementation of the await statement (i) using only locks; (ii) using locks and condition variables. For each of the options, explain when, do you think, it is better to use the option.

1. Wait using only locks:

Drawback: busy-waiting for condition (waste of cpu time).

Use lock-only implementation when the waiting time is expected to be relative short compared to time it takes to block a thread as this is a expensive op performed by the kernel that requires context switching. Use lock-only when low contention, for example each process executes on separate processors. In case of limited resources this requires time-slicing that anyway requires context switches.

1. Wait using lock and condition variable.

This option shall be used in the case of high contention for limited resources (e.g. single CPU) so that waiting processes can be blocked / yielded and give up CPU. Use this option when the waiting time is expected to be rather long compared to a context switch.

**Q**. Briefly explain what a critical section is.

**A**.

Critical section is a piece of code that must be executed with mutual exclusion (only one thread at a time) with respect to critical sections in other processes that reference the same shared variables. Critical sections becomes a problem when multiple threads/processes simultaneously execute their critical sections. This results in race conditions which alters the results of the program.

For example, if two processes increments a shared variable with a non-atomic instruction this may result in the variable can have a multitude of values after completion.

The solution to this a different types of locks, test-set, ticket, bakery.

**Module IV: Semaphores**

**Q**. Explain “Passing the baton” technique used with semaphores.

**A**. Passing the baton is a synchronization technique used with semaphores. The baton is a mutex binary semaphore which controls access to a shared resource.

When one process decides to signal another process and give the mutex, it signals a (signalling) semaphore on which that process is waiting, the signalling process does not release the mutex. This signal has the effect of passing the baton (ownership of mutex) to the signaled process.

This is used in condition synchronization used with mutual exclusion. A signaling prcoess holding the lock signals a signaling semaphore and passes the lock ownership and right to execute with mutual exclusion to the second process. The signaled process becomes the owner of the lock and is responsible for releasing the lock or pass it long to another process.

**Q**. Write pseudo-code illustrating usage of semaphores for (i) mutual exclusion for critical sections (ii) condition synchronization (iii) 2-process barrier synchronization.

1. Sem mutex = 1; P(mutex); critical section; V(mutex);
2. <await(B)> using passing the baton

Sem m = 1, s = 0; boolean B = false;

Waiting process; P(m); while(!B) { V(m); P(s); } V(m)

Signalling process; P(m); B = true; P(s);

1. Sem ar1 = 0, ar2 = 0;

Process1 { …; V(ar1); P(ar2); … }

Process2 { …; V(ar2); P(ar1); … }

**Q.** What is a semaphore?

**A.** Semaphore is a special kind of shared in integer variable which can be only accessed using only two operations: P: <await (s > 0) s = s – 1> and V: < s = s + 1 >

The P operation waits until the semaphore is > 0 and then automatically decrements it.

The V operation increments the semaphore and resumes a process (if any) waiting on the semaphore.

**Module V: Monitors**

**Q**. What is a monitor?

A monitor is a synchronization construct that allows threads to have both mutual exclusion and the ability to wait (block) for a certain condition to become false. Monitor is a thread-safe class, object, or module that wraps around a mutex in order to safely allow access to a method or variable by more than one thread. A monitor is in Java implemented with the *synchronized* keyword along with the constructs wait(), signal() and signalAll().

**Module VI: Message Passing; RPC and Rendezvous**

**Q**. Shortly describe the relative advantages of synchronous and asynchronous message passing, RPC and rendezvous with respect to each other.

**A**.

Synchronous:

* Ensures that the message has been arrived
* Channels can be implemented without extra buffering
* Determistic programming model – could be advantage

Asynchronous:

* More convenient for programming, more powerful programming model:¨
* Wildcard receive -> non-determistic – could be advantage

RPC and rendezvous:

* High-level API; more convient communication mechanisms for client/server applications.

Rendezvous vs RPC;

* Rendezvous calls are accepted, selected and served by an existing process that allows avoiding interference in processing of different calls that might occur in RPC where calls are processed by separate processes, However, RPC might have more concurrency.